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CONFIRMATION NO. 2516

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.		
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RULE						
APPLICANTS Kousuke Tanaka, Chikusei-shi, JAPAN; Hironori Ishizaka, Chikusei-shi, JAPAN; Kouji Tasaki, Chikusei-shi, JAPAN; Masahito Shibutani, Chikusei-shi, JAPAN; Masahisa Shinzawa, Chikusei-shi, JAPAN; Hidehiko Tonotsuka, Chiyoda-ku, JAPAN; Katsuya Iwata, Chiyoda-ku, JAPAN;						
** CONTINUING DATA ***** This application is a 371 of PCT/JP05/00095 01/07/2005						
** FOREIGN APPLICATIONS ***** JAPAN 2004-008313 01/15/2004						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/30/2007						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/Shaun Campbell/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 4	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 1
ADDRESS ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800 ARLINGTON, VA 22209-3873 UNITED STATES						
TITLE MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT LAYERS ELECTRICALLY CONNECTED TO THE ELECTRODES						
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		